



SWARNANDHRA

COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

Accredited by National Board of Accreditation, AICTE, New Delhi, Accredited by NAAC with "A" Grade – 3.32 CGPA, Recognized under 2(f) & 12(B) of UGC Act 1956, Approved by AICTE, New Delhi, Permanent Affiliation to JNTUK, Kakinada Seetharampuram, W.G.D.T., Narsapur-534280, (Andhra Pradesh)

DEPARTMENT OF INFORMATION TECHNOLOGY

TEACHING PLAN

Course Code	Course Title	Semester	Branch	Contact Periods /Week	Academic Year	Date of commencement of Semester
23AM4T01	COMPUTER ORGANIZATION	IV	CSE-DS	6	2025-2026	10-12-2025

COURSE OUTCOMES

1	Identify the basic structure of computer organization(K1)
2	Demonstrate various instructions and arithmetic operations(K3)
3	Illustrate knowledge of functional components on central processing unit and various control units(K2)
4	Determine different memory components in a computer for better memory organization(K4)
5	Explain different ways of communication with I/O devices and standard I/O interface (K2)

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UNIT	Out Comes / Bloom's Level	Topics No.	Topics/ Activity	Text Book/ Reference	Conta ct Hour	Delivery Method
I	CO – 1	1.1	Basic Structure of Computers:	T2	1	Chalk & Board Power point presentations Assignment Test
		1.2	Computer Types	T2	1	
		1.3	Functional units	T2	1	
		1.4	Bus structures	T2	1	
		1.5	Register Transfer and Micro operation	T2	1	
		1.6	Register Transfer Language	T2	1	
		1.7	Register Transfer	T2	1	
		1.8	Bus and Memory Transfers	T2	1	
		1.9	Arithmetic Micro operations	T1	1	
		1.10	Logic Micro operations	T1	1	
		1.11	Shift Micro operations	T1	1	
		1.12	Arithmetic Logic Shift Unit.	T1	1	
Content beyond syllabus		1.13	Floating point arithmetic operations	T1	1	
Total					13	
II		2.1	Basic Computer	T1	1	