|  |  |  |  |
| --- | --- | --- | --- |
| **S NO** | **QUESTION** | **KNOWLEDGE**  **LEVEL** | **CO** |
| **UNIT I** | | | |
| **1** | List the differences between RISC and CISC processors | K1 | CO1 |
| **2** | Explain briefly about the general bus operation of 8085? | K3 | CO1 |
| **3** | Describe how the registers used to access memory. | K2 | CO1 |
| **4** | Explain different registers used in 8086. | K3 | CO1 |
| **UNIT 2** | | | |
| 1 | Explain the function of Flag register? Describe about the each flag bit. | K1 | CO1 |
| 2 | Explain various addressing modes of 8086 with examples. | K3 | CO1 |
| 3 | Explain the maximum mode operation of 8086 processor with a neat schematic diagram. | K3 | CO1 |
| 4 | Define the instruction set of 8086 microprocessor with examples.  a) POP b) AAA c) CMP d) CWD e) ROL | K1 | CO1 |
| **UNIT 3** | | | |
| **1** | List out the Family of Intel Processors. | K1 | CO1 |
| **2** | Explain the differences between Von-Neumann and Harvard architectures? | K3 | CO1 |
| **3** | List out the addressing modes of 8086. | K1 | CO1 |
| **4** | Sketch and briefly explain 8086 architecture. | K3 | CO1 |
| **UNIT 4** | | | |
| **1** | Explain mode-1 and mode-2 operation of 8255. | K2 | CO2 |
| **2** | Describe the typical Stepper motor interfacing with 8255 through 8086. | K1 | CO2 |
| **3** | Explain the internal architecture of 8255 with a neat block diagram. | K2 | CO2 |
| **4** | With neat sketch explain the internal architecture of DMA controller 8257. And also explain Interfacing of 8257 with 8086? | K1 | CO3 |
| **UNIT 5** | | | |
| **1** | Explain by drawing the architecture of 8255 PPI | K1 | CO2 |
| **2** | Explain what are the various steps involved in Stepper motor interfacing? | K1 | CO2 |
| **3** | Explain operational mode of mode 0 in 8255 PPI? | K1 | CO2 |
| **4** | Explain operational command words of 8259. | K1 | CO3 |
| **UNIT 6** | | | |
| **1** | Explain 8051 architecture with neat diagram | **K1** | CO3 |
| **2** | Explain various addressing modes of 8051 with examples. | **K2** | CO3 |
| **3** | Describe what are Assembler Directives of 8051? Explain 4 assembler directives in detail? | **K1** | CO4 |
| **4** | Explain various types of jump instructions in 8051 | **K3** | CO4 |